Standard for IP-XACT, Standard Structure for Packaging, Integrating, and Reusing IP within Tool Flows


This standard describes an eXtensible Markup Language (XML) schema for meta-data documenting intellectual property (IP) used in the development, implementation, and verification of electronic systems. This schema provides both a standard method to document IP that is compatible with automated integration techniques and a standard method (generators) for linking tools into a system development framework, enabling a more flexible, optimized development environment. Tools compliant with this standard will be able to interpret, configure, integrate, and manipulate IP blocks that comply with the IP meta-data description. The standard is independent of any specific design processes. It does not cover those behavioral characteristics of the IP that are not relevant to integration.

Changes in scope: This standard describes an eXtensible Markup Language (XML) schema for meta-data documenting intellectual property (IP) used in the development, implementation, and verification of electronic systems and an application programming interface (API) to provide tool access to the meta-data. This schema provides both a standard method to document IP that is compatible with automated integration techniques. The API provides a standard method (generators) for linking tools into a system development framework, enabling a more flexible, optimized development environment. Tools compliant with this standard will be able to interpret, configure, integrate, and manipulate IP blocks that comply with the IP meta-data description. The standard is based on version 1.4 IP-XACT of The SPIRIT Consortium. The standard is independent of any specific design processes. It does not cover those behavioral characteristics of the IP that are not relevant to integration.

5.3 Is the completion of this standard dependent upon the completion of another standard: No

5.4 Purpose: This standard enables the creation and exchange of IP in a highly automated design environment.

5.5 Need for the Project: As designs get larger and more complex, the electronics industry is using more IP blocks in those designs. The lack of a standard description of those blocks makes their use difficult, error-prone, and costly to implement. This revision will further address these
issues and will update IEEE Std 1685-2009 based on learning from the use of the standard.

5.6 Stakeholders for the Standard: Stakeholders for this standard include Electronic Design Automation (EDA) vendors, IP vendors, electronic systems builders and IC manufacturers.

**Intellectual Property**

6.1.a. Is the Sponsor aware of any copyright permissions needed for this project?: Yes
If yes please explain: The standard will be based on material developed by The Accellera Systems Initiative. They have agreed to transfer the copyright of this material to the IEEE for use in this proposed revision.

6.1.b. Is the Sponsor aware of possible registration activity related to this project?: No

7.1 Are there other standards or projects with a similar scope?: No

7.2 Joint Development
Is it the intent to develop this document jointly with another organization?: No

8.1 Additional Explanatory Notes (Item Number and Explanation): 5,2, 5.5: This revision will update IEEE Std 1685-2009 based on learning from the use of the standard.