P1012-2016/Cor 1

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Type of Project: Corrigendum to IEEE Standard 1012-2016
PAR Request Date: 26-Sep-2016
PAR Approval Date: 07-Dec-2016
PAR Expiration Date: 31-Dec-2020
Status: PAR for a Corrigendum to an existing IEEE Standard
Root Project: 1012-2016

1.1 Project Number: P1012-2016/Cor 1
1.2 Type of Document: Standard
1.3 Life Cycle: Full Use

2.1 Title: Standard for System, Software and Hardware Verification and Validation - Corrigendum 1

3.1 Working Group: Std for Software Verification and Validation Working Group (C/S2ESC/1012_WG)
Contact Information for Working Group Chair
  Name: Roger Fujii
  Email Address: rogerfujii@cox.net
  Phone: 310-567-0702
Contact Information for Working Group Vice-Chair
None

3.2 Sponsoring Society and Committee: IEEE Computer Society/Software & Systems Engineering Standards Committee (C/S2ESC)
Contact Information for Sponsor Chair
  Name: Paul Croll
  Email Address: pcroll@computer.org
  Phone: 540-644-6224
Contact Information for Standards Representative
None

4.1 Type of Ballot: Individual
4.2 Expected Date of submission of draft to the IEEE-SA for Initial Sponsor Ballot: 01/2017
4.3 Projected Completion Date for Submittal to RevCom
Note: Usual minimum time between initial sponsor ballot and submission to Revcom is 6 months.: 05/2017

5.1 Approximate number of people expected to be actively involved in the development of this project: 15
5.2.a. Scope of the complete standard: This verification and validation (V&V) standard is a process standard that addresses all system and software life cycle processes including the Agreement, Organizational Project-Enabling, Project, Technical, Software Implementation, Software Support, and Software Reuse process groups. This standard is compatible with all life cycle models (e.g., system, software, and hardware); however, not all life cycle models use all of the processes listed in this standard. V&V processes determine whether the development products of a given activity conform to the requirements of that activity and whether the product satisfies its intended use and user needs. This determination may include the analysis, evaluation, review, inspection, assessment, and testing of products and processes
5.2.b. Scope of the Proposed changes: Corrections to references, figures, processes and tables.

5.3 Is the completion of this standard dependent upon the completion of another standard: No
5.4 Purpose: The purpose of the standard is to perform the following:
   - Establish a common framework of the V&V processes, activities, and tasks in support of all system, software, and hardware life cycle processes.
   - Define the V&V tasks, required inputs, and required outputs in each life cycle process.
Changes in purpose: The purpose of the standard is to perform the following: - Establish a common framework of the V&V processes, activities, and tasks in support of all system, software, and hardware life cycle processes. - Define the V&V tasks, required inputs, and required outputs in each life cycle process.
5.5 Need for the Project: Correct internal references, update acronym for N/R, correct reference to N/R in a figure, correct name of process/activity/task, and correct entries in Tables 2b and 2c.

5.6 Stakeholders for the Standard: Organizations developing systems with software and hardware components that require an analysis, evaluation, review and testing of the system, software and hardware development products during the development life cycle.

Intellectual Property
6.1.a. Is the Sponsor aware of any copyright permissions needed for this project?: No
6.1.b. Is the Sponsor aware of possible registration activity related to this project?: No

7.1 Are there other standards or projects with a similar scope?: No
7.2 Joint Development
   Is it the intent to develop this document jointly with another organization?: No

8.1 Additional Explanatory Notes: